B.Sc.(CA)
SEMESTER – I

Subject Code – CC-02 **Core Course:** Computer Architecture

Full Marks: 60 **Pass Marks:** 30(including MSE) **Time: 3 hrs**

General Instructions:

i. **Group A :** Carries very short type compulsory questions.
ii. **Group B :** Answer 3 out of 5 subjective/descriptive questions.
iii. Answer in your own words as far as practicable.
iv. Answer all sub parts of a question at one place.
v. Numbers in right indicate full marks of the question.

Group - A

1. **[10 x 1= 10]**

I. What is Input Device?

II. What is Output Device?

III. What is Hexadecimal number system?

IV. What is Instruction Set?

V. What is RISC?

VI. What is Secondary Memory?

VII. What is Register?

VIII. What is Multiplexer?

IX. What is general purpose register?

X. What is the Combinational circuit?

2. What is the use of Boolean algebra in computer? **[05]**

Group - B **[03X15]**

3. Explain Full Adder.

4. Differentiate RAM & ROM.

5. What is meant by triggering of flip-flops.

6. What are the various registers of 8085.

7. Draw the block diagram and explain a ripple counter.

8. What is the essence of Cache memory?